

Claims

- [c1] A semiconductor integrated circuit, comprising:
circuit blocks composed by CMOS process;
analog control lines connected to said circuit blocks;
wherein said analog control lines are wired outside the layout of said circuit blocks so that said circuit blocks and said analog control lines would not be overlapped by the same or the different wiring layers.
- [c2] A semiconductor integrated circuit, comprising:
a plurality of circuit blocks composed by CMOS process, which have ON/OFF functions of the power source ;
analog control lines used to control ON/OFF functions for said power source, which is connected to said plurality of circuit blocks;
wherein said analog control lines are wired outside the layout of said plurality of circuit blocks so that said plurality of circuit blocks and said analog control lines would not be overlapped by the same or the different wiring layers.
- [c3] A semiconductor integrated circuit, comprising on the same chip:
a plurality of circuit blocks composed by the CMOS pro-

cess, which have ON/OFF functions of the power source;
a control circuit to control the ON/OFF functions of the
power source of said plurality of circuit blocks; and
analog control lines connected between said plurality of
circuit blocks and said control circuit;
wherein said analog control lines are wired outside the
layout of said plurality of circuit blocks; or on the layout
of a certain circuit block, analog control lines to another
circuit block are wired;
when the power source of said certain circuit block is
turned ON by said control circuit, said another circuit
block is not controlled in a state of being ON simultane-
ously therewith.

- [c4] A semiconductor integrated circuit composed by CMOS
structure, comprising:
an analog circuit with feedback loop;
wherein an analog signal line for said feedback is wired
outside the layout of said analog circuit.